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13 SEMICONDUCTOR, INC.
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15 UNITED STATES DISTRICT COURT
16 NORTHERN DISTRICT OF CALIFORNIA
17 SAN FRANCISCO DIVISION
18

19 ALPHA & OMEGA
20 SEMICONDUCTOR, INC., a
California corporation; and ALPHA &
21 OMEGA SEMICONDUCTOR, LTD., a
Bermuda corporation,

22 Plaintiffs,

23 v.

24 FAIRCHILD SEMICONDUCTOR
25 CORP., a Delaware corporation,

26 Defendant.

27 AND RELATED COUNTERCLAIMS
28

Case No. 07-2638 JSW (EDL)
(Consolidated with Case No. 07-2664 JSW)

**DECLARATION OF FRANCOIS
HEBERT IN SUPPORT OF
PLAINTIFF'S REPLY IN SUPPORT OF
ITS MOTION TO STRIKE
FAIRCHILD'S PATENT L.R. 3-1
DISCLOSURES**

Date: November 27, 2007
Time: 9:00 a.m.
Location: Courtroom E, 15th Floor
Judge: Hon. Elizabeth D. Laporte

HEBERT DECLARATION IN SUPPORT OF AOS'S REPLY IN
SUPPORT OF ITS MOTION TO STRIKE CASE NO. 07-2638
JSW (EDL) (CONSOL. WITH CASE NO. 07-2664 JSW)

1 I, François Hébert, declare as follows:

2 1. I am the Chief Technology Officer for Alpha & Omega Semiconductor, Inc.

3 2. My educational background includes a Bachelor's degree in Electrical Engineering
4 in 1984 from the University of Waterloo, a Master's degree in Electrical Engineering in
5 1985 from the University of Waterloo and a Ph.D in Electrical Engineering from the
6 University of Waterloo in 1988.

7 3. I have worked in the semiconductor industry for more than 20 years. I have
8 worked in the power MOSFET industry for more than 12 years, and I have extensive
9 knowledge regarding the structure, design, simulation, manufacturing, characterization
10 and performance of these devices.

11 4. I have reviewed Fairchild Corporation's Opposition to Plaintiff's Motion to Strike
12 Fairchild's Patent Local Rule 3-1 Disclosure, as well as the Declaration of Dr. Richard
13 Blanchard in support of that opposition. I may refer to those documents herein as
14 "Fairchild Opposition" and "Blanchard Declaration."

15 5. I disagree with a number of statements in the Blanchard Declaration. For example,
16 I disagree with his statement and suggestions that all of AOS's accused power MOSFET
17 products are manufactured using processes that are the same in all respects relevant to
18 Fairchild's asserted patents.

19 6. In fact, there are numerous differences among AOS's MOSFET devices. For
20 example, different AOS MOSFET products have different turn-on (threshold) voltages,
21 different gate voltage ratings (maximum V_{gs} voltage which can be applied for safe
22 operation), different drain voltage ratings (maximum V_{ds} voltage which can be applied
23 for safe operation), different maximum drain current ratings, different drain to source
24 resistance, different transconductance, different drain to source breakdown voltages
25 (BV_{dss}), different capacitances (input capacitance C_{iss} , output capacitance C_{oss} and gate-
26 drain capacitance C_{rss}), different gate resistance (R_g), different optimum operating
27 frequencies.

28 7. Many of these characteristics relate to the operating performance of the products,

1 including, for example, the resistance of the device to breakdown.

2 8. To achieve the different performance characteristics described above, AOS's
3 MOSFET devices have different features, such as different silicon substrates (n type, p
4 type, and different doping concentrations), different drain drift regions, different doping
5 concentrations in certain regions, carrier type (n or p) and thickness, different trench
6 depth, different trench width, different distances between the trenches, different active cell
7 geometries (striped, closed-cell for example), different depths of the various doping
8 profiles (sometimes referred to as wells), and different number of steps used to fabricate
9 the devices. While not all of the features are different between every product, there
10 are certainly some differences between the products.

11 9. Power MOSFETs are configured and optimized for different applications. AOS's
12 catalog (see aosmd.com website for example), shows that the applications include for
13 example: low-frequency DC-DC power conversion, high-frequency DC-DC power
14 conversion, switch-mode-power-supplies (SMPS), SMPS low-side MOSFET, SMPS
15 high-side MOSFET, low-frequency load-switching applications, battery protection
16 applications, AC-DC power conversion, inverters, motor control, general purpose. Some
17 applications even include multiple MOSFETs in one package, or one MOSFET with one
18 passive device such as a diode. An example showing multiple applications of AOS
19 MOSFETs can be found below (from the AOS website, aosmd.com):
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ALPHA & OMEGA SEMICONDUCTOR			MOSFET Selector Guide - All Products														
Part Number	Status	Replacement Part	Package	Configuration	Popular Application	Typ	ESD Diode	Schottky Diode	Schottky Type	V _{GS} (V)	V _{DS} (V)	I _{AS} (A)	P _D (W)	R _{DS(on)} (mΩ)			
309 AOD496	New		TO-252	Single	SMPS	N	No	No		30	20	82	44	62.5 31 9.5 16			
310 AOD603	Full Production		TO-252-5L	Complementary	inverter	N	No	No		60	20	12	12	2 1.3 60 85			
311 AOD603	Full Production		TO-252-5L	Complementary	inverter	P	No	No		-60	20	-12	-10	2.5 1.6 115 150			
312 AOD604	Full Production		TO-252-5L	Complementary	inverter	N	No	No		40	20	8	8	2 1.3 33 47			
313 AOD604	Full Production		TO-252-5L	Complementary	inverter	P	No	No		-40	20	-8	-8	2.5 1.6 50 70			
314 AOD606	New		TO-252-4L	Complementary	inverter	N	No	No		40	20	8	8	4.2 2.7 33 47			
315 AOD606	New		TO-252-4L	Complementary	inverter	P	No	No		-40	20	-8	-8	5 3.2 50 70			
316 AOD607	New		TO-252-4L	Complementary	inverter	N	No	No		30	20	12	12	2.1 1.3 25 34			
317 AOD607	New		TO-252-4L	Complementary	inverter	P	No	No		-30	20	-12	-12	2.1 1.3 37 62			
318 AOD608	Full Production		TO-252-4L	Complementary	inverter	N	Yes	No		40	20	10	10	2 1.3 39 50			
319 AOD608	Full Production		TO-252-4L	Complementary	inverter	P	Yes	No		-40	20	-10	-10	2.5 1.8 51 75			
320 AQL452	New		TO-251A	Single	General Purpose	N	No	No		25	20	55	50		8.7 14.7		
321 AQL472	New		TO-251A	Single	General Purpose	N	No	No		25	20	50	50		5 9.5		
322 AQL1401	New		Ultra S08	Single	Battery Protection	P	Yes	No		-38	25	-19	-15	5 3 10			
323 AQL1408	Not for new designs	AOL1700	Ultra S08	Single	SMPS Low Side	N	No	No		30	20	27	22	5 3 4	6		
324 AQL1412	New		Ultra S08	Single	SMPS Low Side	N	No	Yes	SRFET	30	12	27	21	5 3 3.9	4.6		
325 AQL1413	New		Ultra S08	Single	Load Switch	P	Yes	No		-30	25	-14	-11	5 3.2	17		
326 AQL1414	Full Production		Ultra S08	Single	SMPS High Side	N	No	No		30	12	21	17	5 3 6.5	7.5		
327 AQL1418	Full Production		Ultra S08	Single	SMPS High Side	N	No	No		30	20	21	17	5 3 6.5	10.5		
328 AQL1420	Not for new designs	AOL1700	Ultra S08	Single	SMPS Low Side	N	No	No		30	20	29	23	5 3 3.7	5.5		
329 AQL1424	New		Ultra S08	Single	SMPS	N	Yes	No		30	20	24	19	5 3 5.4	8		
330 AQL1426	New		Ultra S08	Single	SMPS High Side	N	No	No		30	12	15	12	4 2.6	10.5 13.5		
331 AQL1428	New		Ultra S08	Single	SMPS High Side	N	No	No		30	20	18	14	5 3 9.5	15		
332 AQL1432	New		Ultra S08	Single	SMPS High Side	N	No	No		25	20	21	17	6 4 9.5	14		
333 AQL1436	Not for new designs	AOL1428	Ultra S08	Single	SMPS Low Side	N	No	No		25	30	20	16	5 3 11.5			
334 AQL1440	New		Ultra S08	Single	SMPS Low Side	N	No	No		25	30	25	20	5 3 5.2			
335 AQL1444	Not for new designs	AOL1700	Ultra S08	Single	SMPS Low Side	N	No	No		30	20	26	21	5 3 4.3	6.3		
336 AQL1446	Not for new designs	AOL1418	Ultra S08	Single	SMPS High Side	N	No	No		30	20	21	16	5 3 7	11		
337 AQL1454	New		Ultra S08	Single	inverter	N	Yes	No		40	20	17	13	5 3.2	9 13		
338 AQL1700	New		Ultra S08	Single	SMPS Low Side	N	No	Yes	SRFET	30	20	26	21	5 3.2	4.2 6		
339 AQL1702	New		Ultra S08	Single	SMPS Low Side	N	No	Yes	SRFET	30	12	21	17	5 3.2	5.8 7.2		
340 AQL1704	New		Ultra S08	Single	SMPS Low Side	N	No	Yes	SRFET	30	12	18	14	4.3 2.8	7.8 9.8		
341 AQL1708	New		Ultra S08	Single	SMPS Low Side	N	No	Yes	SRFET	30	20	21	16.5	5 3.2	6.2 9.5		
342 AON3402	New		DFN 3x3	Single	General Purpose	N	Yes	No		20	12	12	9.6	3 1.9	13 17		
343 AON3406	New		DFN 3x3	Single	General Purpose	N	Yes	No		30	30	10	7.8	3 1.9	15 24		

10. Specific differences between transistors configured for different applications include, without limitation, the following examples: Devices which are switched often and under severe conditions (SMPS, DC-DC converters, etc...) require different characteristics than devices which are rarely switched. Motor-control devices (devices used in hand held battery powered tools for example), must be able to survive much harsher conditions than MOSFETs used to protect batteries of notebook computers and as a result, special techniques to clamp the breakdown voltage and protect the active cells of Power Tool MOSFETs are integrated in the structure.

11. In addition to differences between among the 14 products for which Fairchild provided analysis, there are also difference between those 14 products and the other 342 products that Fairchild accuses of infringement.

12. For example, many of the 342 products have performance characteristics not reflected among any of the 14 products. As one example, the highest R_{DS} among the 14 specifically identified products is 52 mΩ (AOD6405), while R_{DS} of the 342 products AOS products (at $V_{GS}=10V$) ranges from 3.5 mΩ (AOD438) to 1600 mΩ (AO5800E).

1 13. Similarly, while the 14 products for which Fairchild provided analysis all have the
2 same drain-source voltage of 30V, the drain-source voltage of the other members in the
3 product family ranges from 12V (AO4437) to 20V (AO6702), 30V (AO6800), 40V
4 (AO4617), 60V (AO4612), 75V (AO4850); AOD464 (105V), AOT404 (105V), and
5 AOD450 (200V).

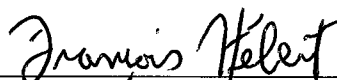
6 14. These differences among drain-source voltages of the AOS products are driven by
7 the technical features of those products. For example, the V_{DS} as well as the R_{DS} (when
8 normalized to the area of the device) are impacted by (among other things) the relative
9 depths of a MOSFET transistor's trenches and wells.

10 15. In short, there are certainly differences among the 14 products for which Fairchild
11 has provided reverse-engineering analyses, as well as differences between those products
12 and the other products listed on the AOS selection guide. Some devices for example, have
13 a well which is shallower than the gate trenches, while other devices have wells which
14 are deeper than the gate trenches.

15 16. Finally, it is my understanding that at least some of these differences between the
16 AOS products would be shown by additional reverse engineering, such as Scanning
17 Electron Microscopy (SEMs), or Scanning Capacitance Microscopy (SCMs), or
18 Secondary Ion Mass Spectroscopy (SIMS). Among other things, such analysis would
19 indicate the relative depths of the trenches and wells in the products.
20

21 I declare under penalty of perjury under the laws of the United States that the
22 foregoing is true and correct to my personal knowledge.
23

24 Executed this 13th day of November, 2007, at Sunnyvale, California.
25

26 By 
27 François Hébert
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